

2131

TRANSMITTAL FORM				Application No.	09/6	568,408	
				Filing Date	Sep	September 22, 2000	
(to be used for all correspondence after initial filing)				First Named Inventor	Car	l M. Ellison	
				Art Unit	213	1	
			Examiner Name	Tag	hi T. Arani		
Total Number of I	Pages	in This Submiss	ion 8	Attorney Docket Number	er 423	90P8628X	
		ENCLO	SURES (chec	k all that apply)	•		
Fee Transmittal	Form		Drawing(s)			After Allowance Communication to Group	
Fee Attac	hed		Licensing-related Papers			Appeal Communication to Board of Appeals and Interferences	
Amendment / Re	espon	se	Petition			Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	
After Fina Affidavits		ration(s)	Petition to (Provisional	Convert a Application		Proprietary Information	
Extension of Tin	ne Red	quest	Power of A Change of	ttorney, Revocation Correspondence Addres	l	Status Letter	
Express Abandonment Request			Terminal D	isclaimer		Other Enclosure(s) (please identify below):	
Information Disclosure Statement			Request for	r Refund	i 1	Cross reference to related application	
PTO/SB/08 Certified Copy of Priority Document(s)			CD, Numbe	er of CD(s)		return postcard 6 cited references	
					[<u></u>	
Response to Missing Parts/ Incomplete Application			Remarks				
Basic Filing Fee Declaration/POA				_			
Response to Missing Parts under 37 CFR 1.52 or 1.53							
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT							
Firm *	Gr	egory D. Cal	dwell, Reg. No	o. 39,926			
			OKOLOFF,	TAYLOR & ZAF	MAN	LLP	
Signature							
Date September 21, 2004							
CERTIFICATE OF MAILING/TRANSMISSION							
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Typed or printed na	Gayle M. Be	kish					
Signature			23	-	Date	September 21, 2004	

OIPE 100, 2668,408

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

e Patent Application of:

Carl M. Ellison et al.

Application No.: 09/668,408

Docket No.: P8628X

Filed: 9/22/00

For: MANAGING A SECURE

ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE

Examiner: Taghi T. Arani

Art Unit: 2131

Phone No.: (703) 305-4274

CROSS REFERENCE TO RELATED APPLICATIONS

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby cross-reference the following related patents and patent applications, which are all assigned to the same assignee as the present patent application.

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Date of Deposit

Caule Bekish

Name of Person Mailing Correspondence

92104

Signature

Date

eference	Application		
No.	Serial No.	Patent No.	
Ţ	09/672,603		Controlling Accesses To Isolated Memory Using A Memor
	00/000 00/		Controller For Isolated Execution
2	09/822,986		Checking File Integrity Using Signature Generated In
3	00/500 051		Isolated Execution
3	09/538,951		Platform And Method For Issuing And Certifying A
4	00/520 244		Hardware-Protected Attestation Key
*	09/539,344		Managing A Secure Platform Using A Hierarchical
5	00/669 595		Executive Architecture In Isolated Execution Mode
J	09/668,585		Managing A Secure Platform Using A Hierarchical
6	09/541,108	 	Executive Architecture In Isolated Execution Mode
7			Platform And Method For Remote Attestation Of A Platfor
,	09/540,612		Platform And Method For Generating And Utilizing A
8	00/540 045		Protected Audit Log
	09/540,946		Protecting Software Environment In Isolated Execution
9	09/668,610	<u>L</u> .	Protecting Software Environment In Isolated Execution
10	09/538,954		Generating Isolated Bus Cycles For Isolated Execution
11	09/539,348	6,760,441	Generating A Key Hierarchy For Use In An Isolated
		, .,,	Execution Environment
12	09/541,477	6,507,904	Isolated Instructions For Isolated Execution
13	09/540,611		
14	09/540,613		Managing Accesses In A Processor For Isolated Execution
	03.0.0,015		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
15	09/668,408		Managing A Secure Facility and It is a Strict of Strict
	111111111111111111111111111111111111111		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
16	09/541,667		
17	09/672,602	~	Attestation Key Memory Device And Bus
18	09/618,738	6 670 005	Attestation Key Memory Device And Bus
10	09/010,/36	6,678,825	Controlling Access To Multiple Isolated Memories In An
19	09/618,489	6 622 062	Isolated Execution Environment
	02/010,469	6,633,963	Controlling Access To Multiple Memory Zones In An
20	10/683,542		Isolated Execution Environment
	10/003,372		Controlling Access To Multiple Memory Zones In An
21	09/618,659		Isolated Execution Environment
	27,010,007		Resetting A Processor In An Isolated Execution Environment
22	09/751,586		
			Resetting A Processor In An Isolated Execution Environment

It may be noted that the above list includes the present application, and the above list partially overlaps the cross-reference to related applications that was filed earlier.

If there are any questions that may be cleared up in a telephone interview, the Examiner is invited to call the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 4/20/04

Michael R. Barré
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Intel Americas, Inc.
Registration No. 44,023
(512) 732-3927

c/o Blakely, Sokoloff, Taylor & Zafman, LLP 12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025-1026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Carl M. Ellison et al.

Application No.: 09/668,408

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Examiner: Taghi T. Arani

Art Unit: 2131

Phone No.: (703) 305-4274

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find an Information Disclosure Statement, which is being submitted after the mailing of the first Office Action but before the mailing of a Final Office Action. A copy of each cited reference is also enclosed.

In accordance with 37 C.F.R. § 1.97(e), I hereby state that no item of information in the Information Disclosure Statement filed herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject

application, and is not to be construed as an admission that the information cited in this statement is material to patentability.

Applicants respectfully request that the Examiner consider the cited reference(s), initial each reference on the enclosed Information Disclosure Statement to indicate such consideration, and return a copy of the initialed Information Disclosure Statement to Applicants. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/2004

Michael R. Barré Patent Attorney Intel Americas, Inc. Registration No. 44,023 (512) 732-3927

c/o Blakely, Sokoloff, Taylor & Zafman, LLP 12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025-1026

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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Sheet 1 of

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(Complete if Known	
Application Number	09/668,408	
Filing Date	September 22, 2000	
First Named Inventor	Carl M. Ellison	
Art Unit	2131	
Examiner Name	Taghi T. Arani	
Attorney Docket Number	42390P8628X	

			U.S. PATE	NT DOCUMENTS	
Examiner Initials*	Cite No.'	Document Number Number - Kind Code² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-5303378	04-12-1994	COHEN	
	2	US-5522075	05-28-1996	ROBINSON et al	
	3	US-5668971	09-16-1997	NEUFELD	
	4	US-5898883	04-27-1999	FUJII et al	
	5	US-5950221	09-07-1999	DRAVES et al	
	6	US-6226749	05-01-2001	CARLOGANU et al	
		US-			
		US-	·		
		US-			

FOREIGN PATENT DOCUMENTS					
	Cite	Foreign Patent Document			Pages, Columns, Lines,
	No.1	Country Code - Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear
	1				

Examiner	Date
Signature	Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number (optional):See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04:Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3):For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possibf&pplicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.